



SWARNANDHRA COLLEGE OF ENGINEERING AND TECHNOLOGY
(Autonomous)

Narsapur, West Godavari District, A.P. 534280

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

BoS Meeting

Dt.:16.07.2024

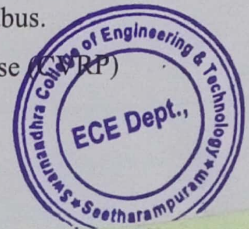
The Board of Studies (BoS) meeting of the ECE department held on 16.07.2024 at 10.30 A.M. through online mode for the review and approval of III and IV SEMS course structure and syllabus.

The following members are attended the meeting

S. No.	Name of the Member	Designation in BoS
1.	Dr. B. Subrahmanyeswara Rao	Chairman
2.	Dr. K. Chandra Bhushana Rao, Director of Academic Planning, JNTUGV, Vijayanagaram	JNTUK University Nominee
3.	Dr. N.N. Murthy, Dean, IIT, Tirupathi	Subject Expert
4.	Dr. B. Kota Naga Srinivasa Rao, Professor, NIT, Warangal	Subject Expert
4.	Mr. V. Govinda Rao, Microlink Technologies, Vijayawada	Industrial Expert
5.	Ms. K. Madhu Harshitha, Software Associate, CTS, Chennai	Alumni Member
6.	Dr. Y.S.V. Raman	Member
7.	Dr. B. Ramana Kumar	Member
8.	Dr. D. Nataraj	Member
9.	Mr. J.E.N. Abhilash	Member
10.	Mr. D. Sekhar	Member
11.	Mrs. G.B. Christina	Member
12.	Mrs. E. Suma	Member

Agenda:

- 1) Review & Approval of R-23 III & IV SEM Course structure and syllabus.
- 2) Exchange of courses III SEM (U.H.V) to IV SEM (M.E.F.A.) and IV SEM (M.E.F.A.) to III SEM (U.H.V).
- 3) Discussion on EDC syllabus with reference to I SEM Basic Electronics syllabus.
- 4) Discussion on STLD syllabus with reference to I SEM Basic Electronics syllabus.
- 5) Replace of ECE III SEM course (P.T.S.P) by Basic Science Mathematics course (P.T.S.P)
- 6) Review on syllabus of all other in III SEM & IV SEM courses.
- 7) Approval of R-24 M, Tech. course structure.





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MINUTES OF BoS MEETING

1. BoS Chairman welcomed the University Nominee Dr. K. Chandhra Bushana Rao, Subject Experts Dr. N.N. Murthy and Dr. B. Kota Naga Srinivasa Rao, Industrial Expert Mr. V. Govinda Rao, Alumni Member Ms. K. Madhu Harshitha and Faculty members.
2. BoS Chairman presented the all above agenda points and later room is given to the experts for discussion and suggestions

Dr. K. Chandra Bhushana Rao, Director of Academic Planning, JNTUGV, Vijayanagaram

University Nominee Suggested the following:

- 1) To delete the repeated topics which are already covered in Basic Electronics course in I st semester and replace them with IoT related electronic devices & components used for IoT Projects.
- 2) To include design of Gates using Universal Gates topics instead of Basic logic Gates in STLD Course.
- 3) Given acceptance for exchange of course III SEM (U.H.V) to IV SEM (M.E.F.A.) and IV SEM (M.E.F.A.) to III SEM (U.H.V).
- 4) To replace the III SEM(PTSP) with Basic Science related Mathematics course and get the approval from Academic Council
- 5) To place the study of IC version topics in laboratory courses.
- 6) To submit any minor changes in few courses like EDC, STLD, EMWTL, EDC lab and STLD lab for approval of BoS members.
- 7) Given approval for all other courses syllabus in III & IV Semesters.
- 8) R-24 M.Tech course structure approved.

Dr. N.N. Murthy, Dean, IIT, Tirupathi, Subject Expert suggested the following:

- 1) To replace the Basic Concepts of EDC with relevant useful advanced topics.
- 2) To add few P.D.C concepts in III unit of E.D.C. which are not covered in IV SEM Courses like E.C.A etc.,
- 3) To include transistor amplifier characteristics in place of CE,CB,CC configurations which are already covered in I SEM Basic Electronics course.
- 4) To delete topic CMOS amplifier in unit 5 of EDC and place in further forth coming courses.
- 5) To place the study of IC version topics in laboratory courses.





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- 7) Given approval for all other courses syllabus in III & IV Semesters.
- 8) R-24 M.Tech course structure approved.

Mr. V. Gvintha Rao, Microlink Technologies, Vijayawada, Industrial Expert:

- 1) Suggested to include Electronics syllabus which is useful for designing latest IOT interfaces used for Project development.
- 2) Suggested to include few more design experiments in laboratory courses.
- 3) Suggested to include all GATE topics in syllabus.
- 4) Expressed other courses syllabus are satisfactory.
- 5) R-24 M.Tech course structure approved.

Ms. K. Madhu Harshitha, Software Associate, CTS, Chennai, Alumni Suggested.

- 1) Instead of basic electronic devices to add Project related electronic devices.
- 2) To add design of Gates using universal gates instead of introduction to logic gates.



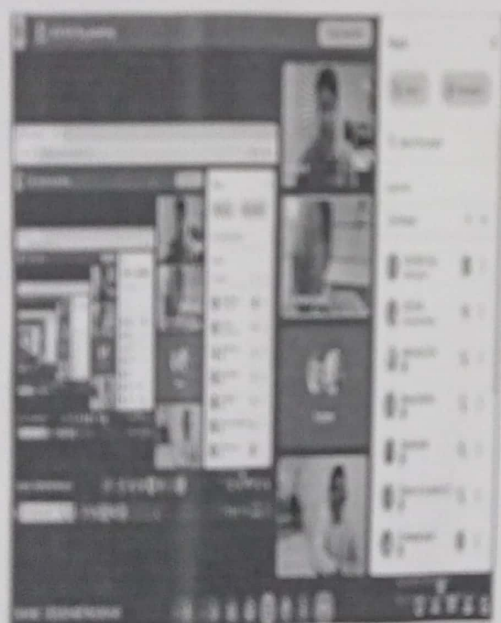
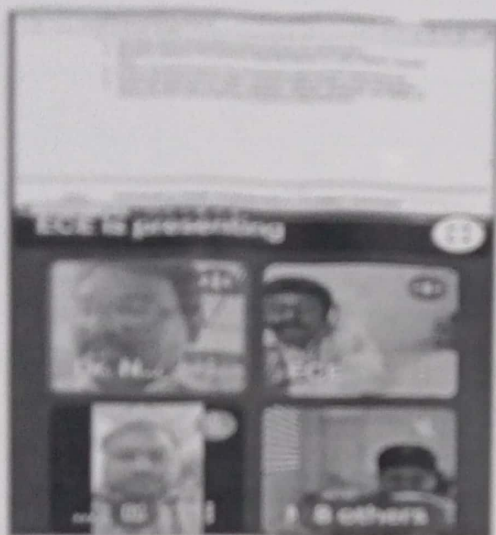


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Dr. B. S. Rao
Dr. B. S. Rao
Chairman BoS,
Department of ECE